/			ional ogi			
11/5	10:30	11:15	Keynote	Kaustav	Banerjee	Sustaining Moore's Law (and Beyond) with 2D Materials
	11:15	11:45	Invited	Kosuke	Nagashio	Two-Dimensional Tunnel FET
	11:45	12:15	Invited	Dong-ming	Sun	MXene film for optoelectronic integration
	12:15	12:30	1105	Shiyu	Zhang	Optoelectronic properties of pseudo-direct transition <i>n</i> <sup>+</sup> -GeSn and heterostructures composed of <i>n</i> <sup>+</sup> -GeSn and <i>n</i> - SiGe thin layers
	13:30	14:30	Poster			
	14:30	15:15	Keynote	Youfan	Hu	Carbon Nanotube-Based Flexible Electronics
	15:15	15:45	Invited	Jana	Zaumzeil	Doping of Sorted Semiconducting Single- Walled Carbon Nanotubes for Low-Power Circuits
	16:00	16:30	Invited	Albert	Nasiblin	Carbon Nanotubes: from Synthesis to Applications
			Invited	Yutaka	Ohno	Design and fabrication of flexible analog/digital mixed-signal circuits based on carbon nanotube thin film transistors Crystalline microstructure and electric
	17:00	17:15	1092	Tetsuya	Tohei	property of rutile TiO <sub>2</sub> single crvstal memristor
	17:15	17:30	1165	Fugo	Nanataki	First-principles studies on the effects of O atoms in the substrate on the oxidation of a vertical Si nanopillar
11/5	13:30	14:30	Poster			
			1036	Wangzhen	Zhao	The Study of Interface Layer In Hybrid Memory Layer for Thermally Assisted STT- MRAM Cells
			1116	Masanori	Endo	A correlation between crystallinity of single- walled carbon nanotubes and temperature coefficient of Raman shift frequency
			1139	Daiki	Oshima	Fabrication of epitaxially grown ß-Mn type CoZnMn film
			1175	Adha Sukma	Aji	CNT/HfO <sub>2</sub> /CNT Memristor for Neuromorphic Computing
			1179	Rikizo	Yano	Magnetically Doped Topological Insulator and its Superconducting Proximity Effects
			1181	Tomoki	Hori	Property Control of Carbon Nanotube Thin- Film Transistors Using Passivation Layer Deposited by Atomic Layer Deposition

Area 2 International Symposium on Nano material-based Electronics (CIRFE Symposium)